## INFORMATION DISCLOSURE CITATION

ATTY DOCKET NO.

APPLICATION NO.

10/518,974

(Use several sheets if necessary)

APPLICANT(S)

Richard Michael Taylor

FILING DATE

**GROUP ART UNIT** 

Dec. 21, 2004

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DJH		WO 03/040916 A1	15 May 2003	PCT	<del></del>		YES	МО			
DJH	В	EP 081 0519 A2	30 May 1997	EP							
DJH	c	EP 1 050 811 A1	03 May 1999								
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DJH		Hideki Ando, Chikako Nakanishi, Tetsuya Hara, Masao Nakaya; "Unconstrained Speculative Execution with Predicated State Buffering"; System LSI Laboratory; Mitsubishi Electric Corporation; 22nd ISCA, pp 126-137, June 1993									
DJH		Dionisios N. Pnevmatikatos; Gurinder S. Sohi; "Guarded Execution and Branch Prediction in Dynamic ILP Processors"; Computer Sciences Department; University of Wisconsin-Madison; pp 120-129									
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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(Use several sheets if necessary)				Richard Michael Taylor							
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DJH	F	Marc Tremblay; Jeffrey Chan; Shailender Chaudhry; Andrew W. Conigliaro; Shing Sheung Tse; "The MAJC Architecture: A Synthesis of Parallelism and Scalability"; November - December, IEEE Micro; pp 12 - 25									
DJH	G	Jenn-Yuan Tsai; Pen-Chung Yew; "Ehancing Multiple-Path Speculative Execution with Predicate Window Shifting"; pp 0 - 27									
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